

A-SSCC 2020 Program-at-a-glance
November 9-11, 2020 (JST, UCT+9)

Day	Date	Time(JST)	Track A	Track B	Track C	Track D
1st Day	Monday, November 9	9:00-10:30	Tutorial I			SDC Exhibition/ FPGA Demo
		10:30-10:50	Break			
		10:50-12:20	Tutorial II			
		12:20-13:40	Lunch			
		13:40-15:10	Tutorial III			
		15:10-15:30	Break			
		15:30-17:00	Tutorial IV			
		17:00-18:45	Break			
		18:45-19:00	TPC Meeting			
2nd Day	Tuesday, November 10	9:00-9:20	Opening Ceremony/ Welcome Remarks			SDC Exhibition/ FPGA Demo
		9:20-10:05	Session 1 - Plenary Talk 1			
		10:05-10:50	Session 1 - Plenary Talk 2			
		10:50-11:10	Break			
		11:10-12:25	Session 2 - Industry Program	Session 3 - FPGA	Session 4 - DC (Data Converters)	
		12:25-13:30	Special Session: TBA			
		13:30-15:35	Session 5 - ACS (Analog Circuits & Systems)	Session 6 - DCS (Digital Circuits & Systems)	Session 7 - RF (Radio Frequency)	
		15:35-15:50	Break			
		15:50-17:30	Session 8 - Panel Discussion			
		17:30-18:30	Break			
		18:30-19:30	Special Q&A Session 1 (separated rooms by each session 2,3,4,5,6,&7)			
		19:30-21:00	Virtual Banquet/Award Ceremony			
3rd Day	Wednesday, November 11	8:30-9:15	Session 9 - Plenary Talk 3			
		9:15-10:00	Session 9 - Plenary Talk 4			
		10:00-10:30	Break			
		10:30-12:35	Session 10 - ACS (Analog Circuits & Systems)	Session 11 - RF (Radio Frequency)	Session 12 - ETA (Emerging Technologies and Applications)	
		12:35-13:40	Special Session: TBA			
		13:40-15:20	Session 13 - ACS (Analog Circuits & Systems)	Session 14 - SOC (SoC & Signal Processing Systems)	Session 15 - WLN (Wireline)	
		15:20-15:50	Break			
		15:50-17:30	Session 16 - DC (Data Converters)	Session 17 - DCS & SOC (Digital Circuits & Systems)(SoC & Signal Processing Systems)	Session 18 - ETA & MEM (Emerging Technologies and Applications)(Memory)	
		17:30-18:30	Break			
		18:30-19:30	Special Q&A Session 2 (separated rooms by each session 10,11,12,13,14,15,16,17&18)			